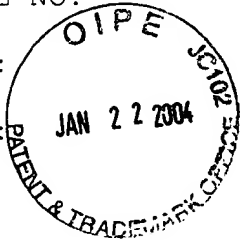


PATENT  
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:                   SCHAUER -1 PCT                   EXAMINER:   T. TRAN  
SERIAL NO:                 09/890,656                   GROUP:       2811  
FILED:                    AUGUST 2, 2001  
TITLE:                    SEMICONDUCTOR WAFER COMPRISING A THIN  
                            EPITAXIAL SILICON LAYER AND METHOD FOR  
                            PRODUCING THE SAME



PETITION UNDER RULE 136(a) AND RULE 17(a)(3)

MAIL STOP FEE AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**COPY**

Dear Sir:

Applicant petitions the Commissioner of Patents and Trademarks to extend the time for response to the Office Action dated July 25, 2003, for three months from OCTOBER 25, 2003 until JANUARY 25, 2004.

A check in the amount of \$ 950.00, covering the fee for a three-month Extension for a Large Entity is enclosed herewith. Any deficiency or overpayment should be charged or credited to deposit Account No. 03-2468.

Respectfully submitted,  
SCHAUER ET AL - 1- PCT

COLLARD & ROE, P.C.  
1077 Northern Boulevard  
Roslyn, New York 11576  
(516) 365-9802  
ERF:djp

\_\_\_\_\_  
Allison C. Collard; Reg. No. 22,532  
Edward R. Freedman; Reg. No. 26,048  
Attorneys for Applicants

Enclosure: 1) Check for \$950.00 for Large Entity

CERTIFICATION UNDER 37 CFR 1.10

I hereby certify that this 37 CFR 1.53 Request and the documents referred to as attached therein are being deposited with the United States Postal Service on this date January 13, 2004 in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR 1.10, Mailing Label No. EV 413 36 3509 US addressed to the: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

\_\_\_\_\_  
Maria Guastella